
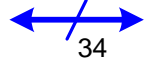
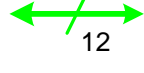
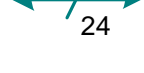

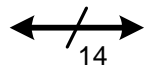
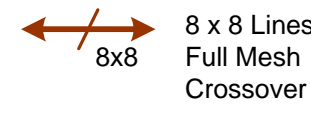
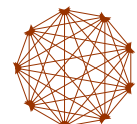

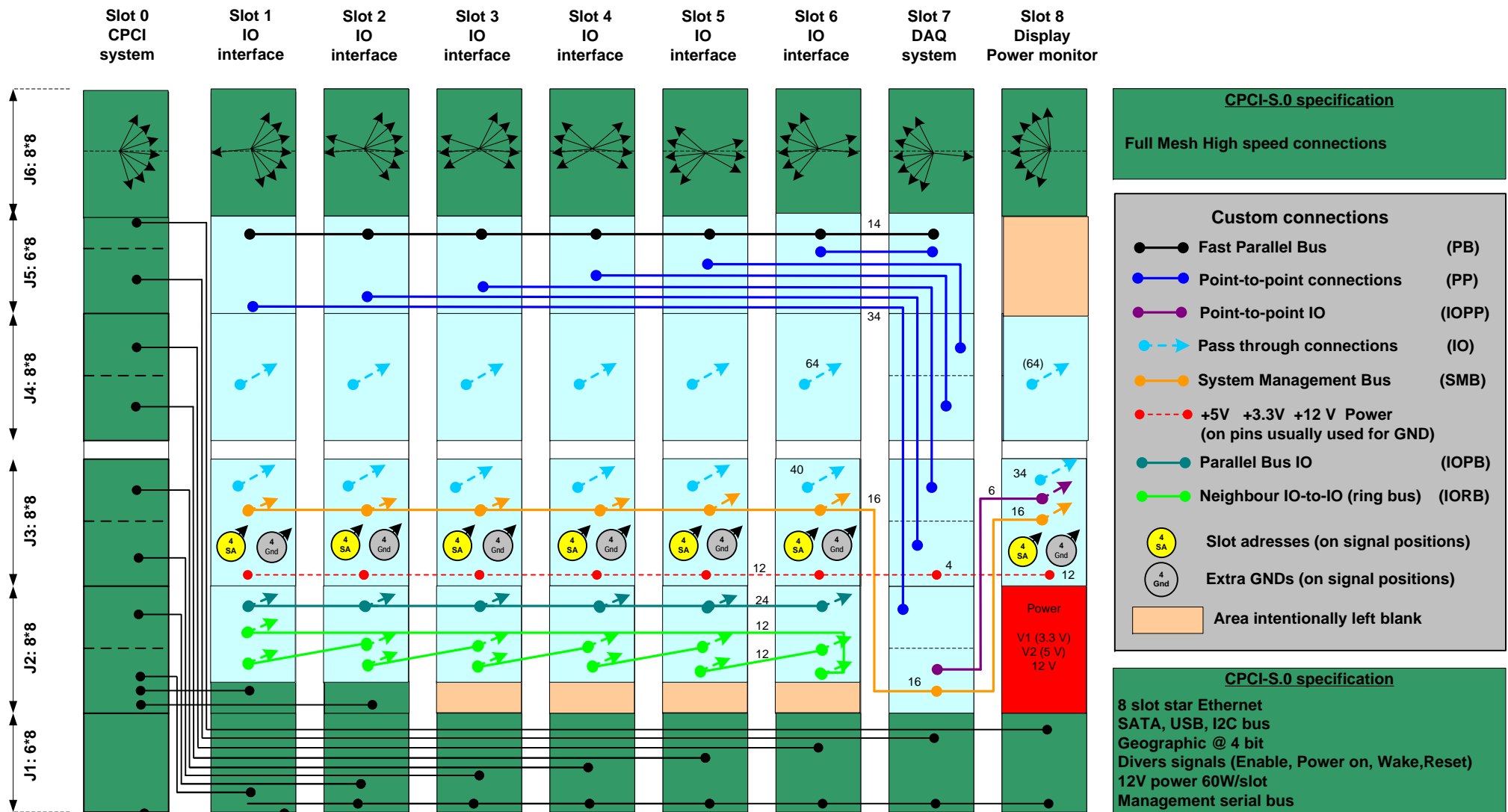


-  104 Pass Through Connections Front – Rear (IO)
-  32 Point to Point slot 7 to slot 1 to 6 (PP)
2 Handshake Signals (Bus Request / Grant)
-  12 Neighbour to Neighbour (IORB)
-  24 Parallel Bus (I/O-cards, slot 1 to 6) (IOPB)

-  Slow System Bus (with Pass Trough, slot 1 to 8) (SMB)
5 Serial System Management (clk, sdi, sdo, 2 x mode)
1 Reset
1 General IO Enable
3 pulled up (Interrupt, Error, ...)
2 Clock Sync (1 x 2 LVDS)
4 Aux
-  Fast Parallel Bus (slot 1 to 7) (PB)
6 Clock (3 x 2 : LVDS clock)
8 General Sync (4 x 2 LVDS: Enable,...)

-  8 x 8 Lines Full Mesh Crossover 
-  6 Point to Point - slot 7 to slot 8 (IOPP)



2nd Gen. SING instrument electronics

DAQ (CPCI Serial) Backplane concept v2.21