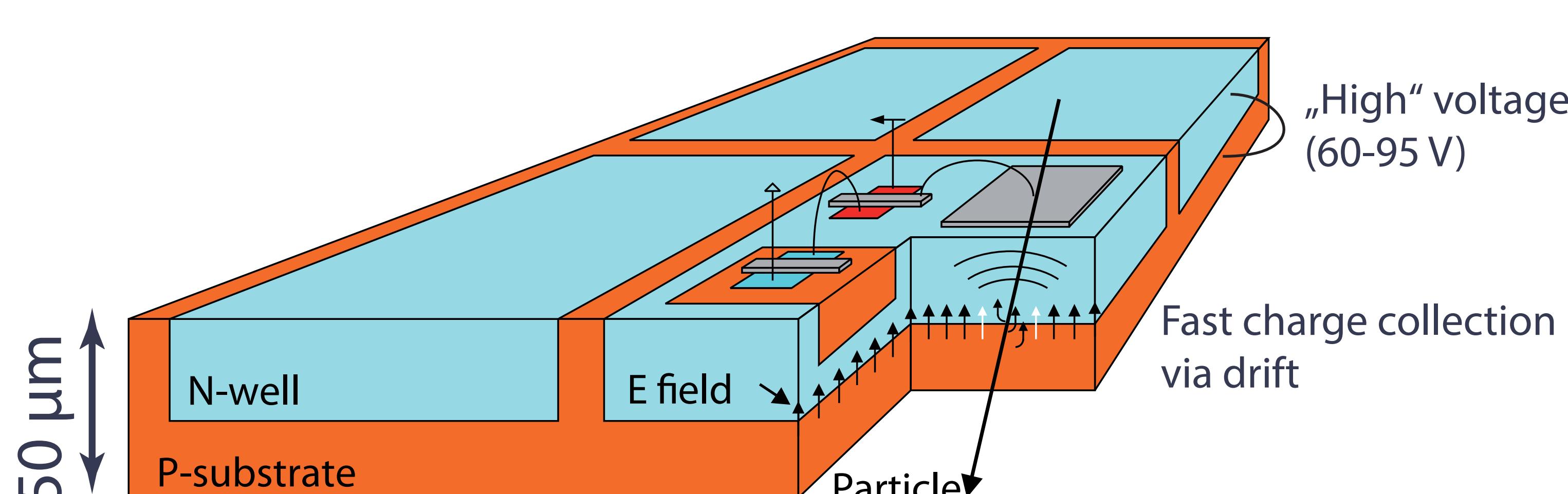


The MuPix7 High-Voltage Monolithic Active Pixel Sensor

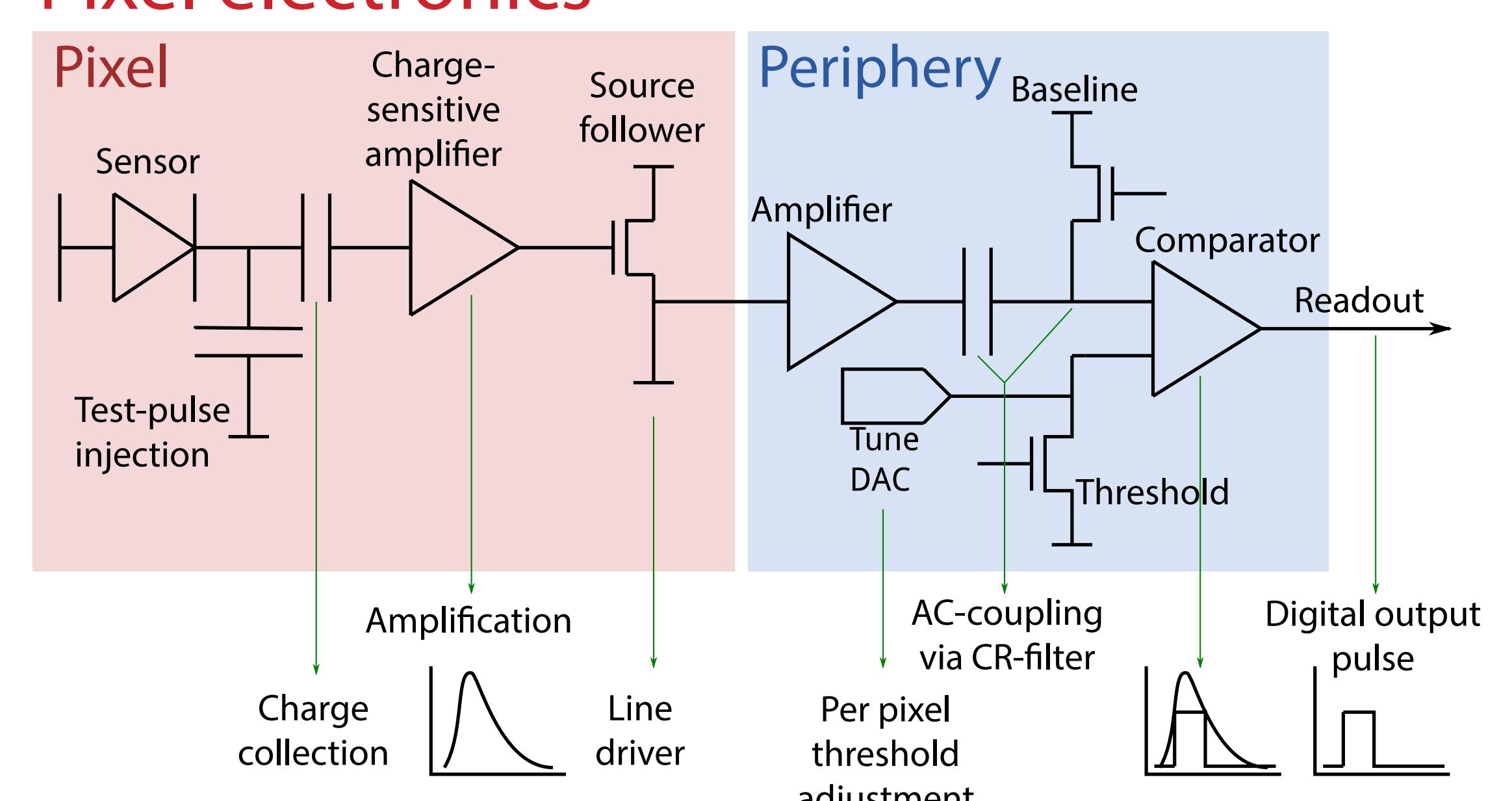
Ivan Perić¹, Shruti Shrestha², Niklaus Berger³

¹ Institut für Prozessdatenverarbeitung und Elektronik, Karlsruher Institut für Technologie (ivan.peric@kit.edu)
² Physikalisches Institut, Universität Heidelberg (shruti@phys.ksu.edu)
³ Institut für Kernphysik, Universität Mainz (nberger@uni-mainz.de)

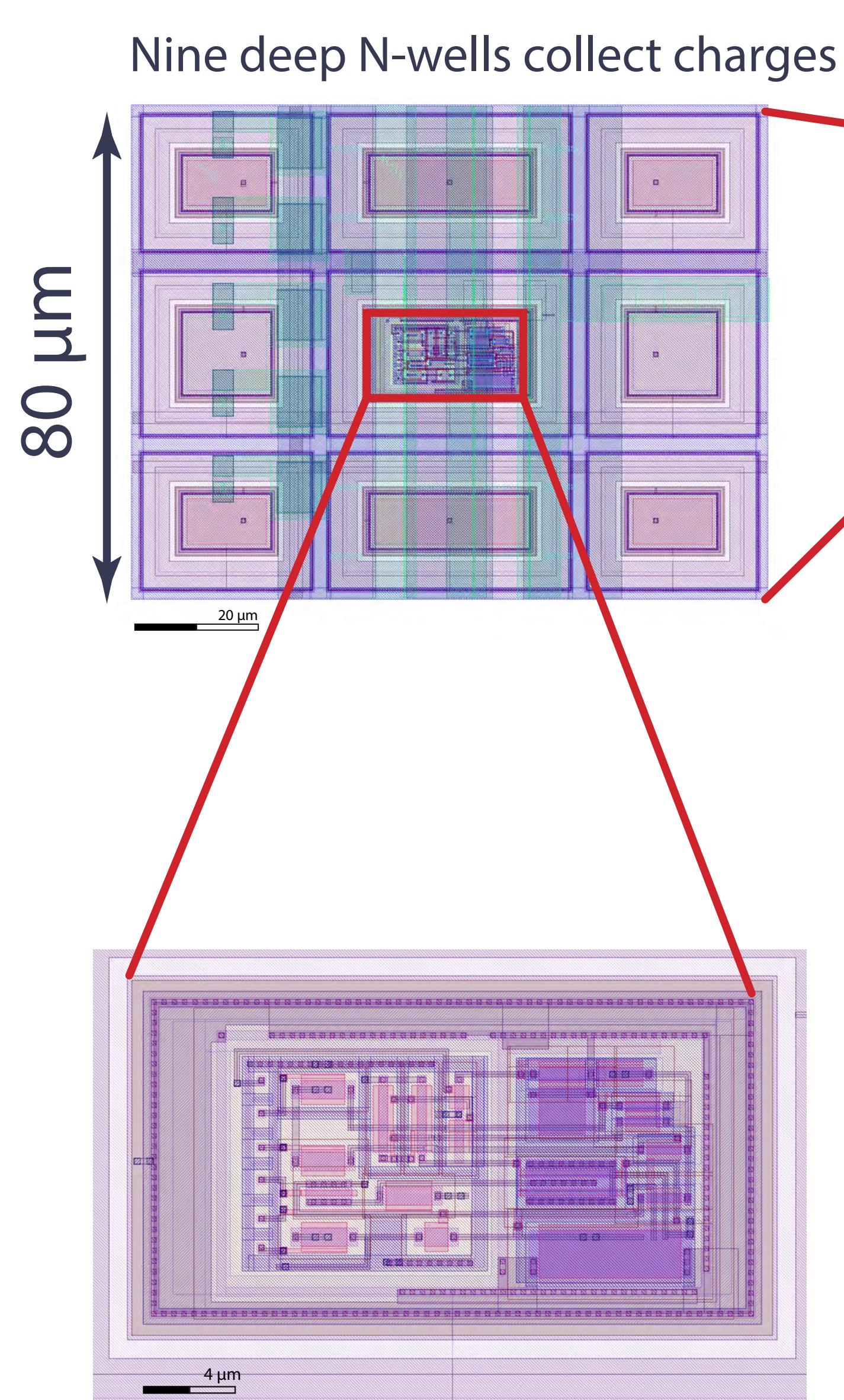
Working principle



Pixel electronics



Pixel Cell



Amplifier

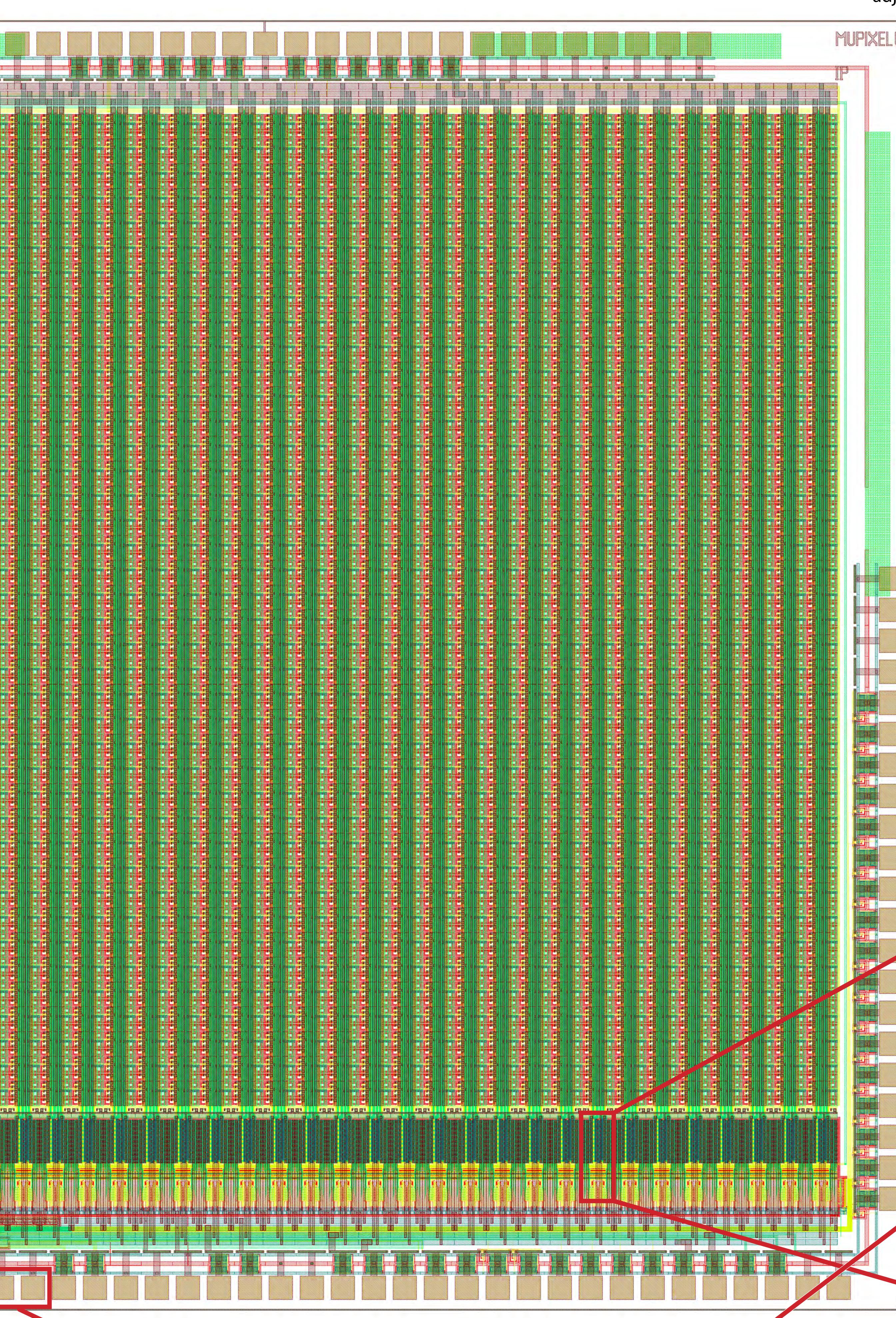
First amplification stage inside each pixel

Digital Control

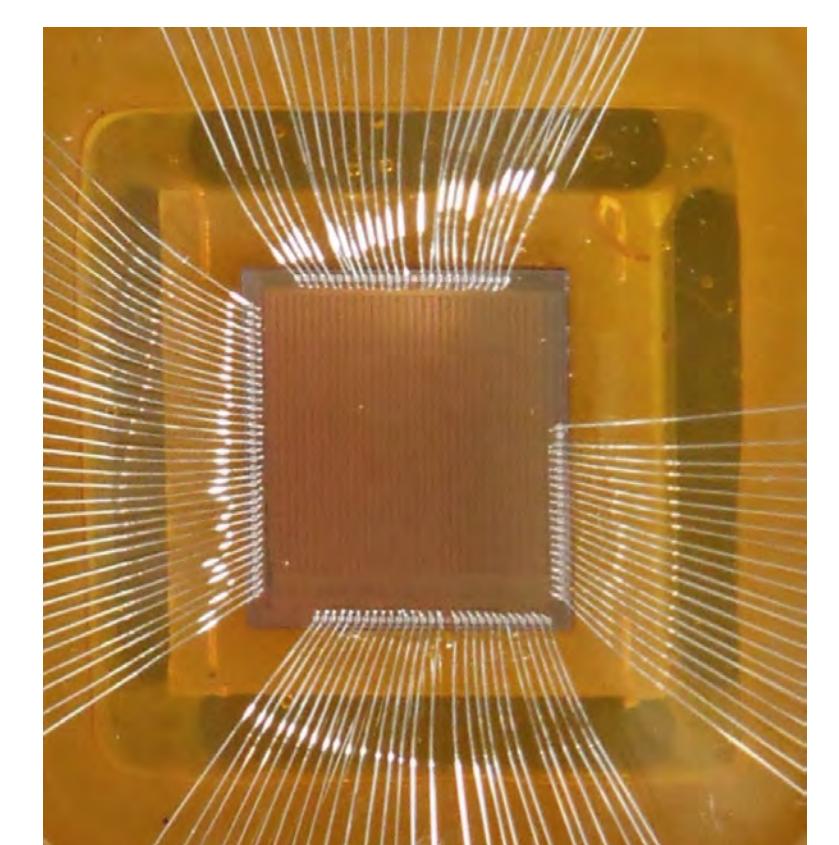
Timestamp generation
Readout control state machine
Data encoding (8bit/10bit)
Synthesized from Verilog code

Clock Generation and Serializer

Voltage-controlled oscillator (VCO)
Phase-locked loop (PLL)
Serializer (up to 1.6 Gbit/s)



Bonded Chip

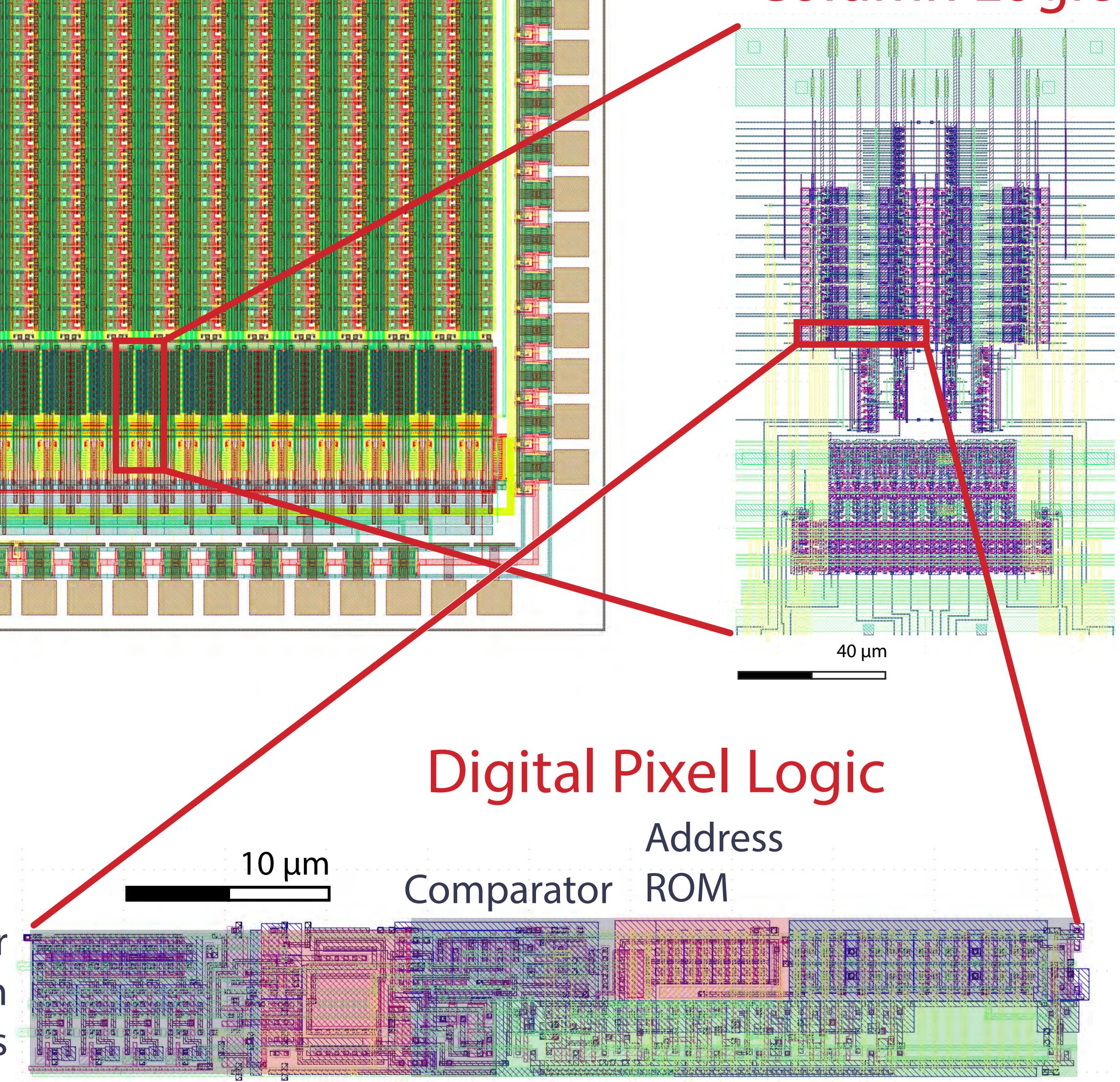


Manufactured in a Austria Microsystems High-Voltage CMOS process with 180 nm feature size

32 x 40 pixels with 80 x 103 μm² size

Thinned to 50 μm

Column Logic



References

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Nucl. Instrum. Meth., 2007, A582, 876

I. Perić, I. and C. Takacs: Large monolithic particle pixel-detector in high-voltage CMOS technology
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Nucl. Instrum. Meth., 2011, A 650, 158

I. Perić, I. et al.: High-voltage pixel detectors in commercial CMOS technologies for ATLAS, CLIC and Mu3e experiments
Nucl. Instrum. Meth., 2013, A731, 131

H. Augustin et al.: The MuPix high voltage monolithic active pixel sensor for the Mu3e experiment
JINST, 2015, 10, C03044



Mu3e Experiment at PSI
www.psi.ch/mu3e



Berger group in Mainz
<http://agberger.kph.uni-mainz.de/>



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Programm

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